



ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

## U.S. REISSUE Patent Application

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APPLICATION NO. 09/771229	CONT/PRIOR D F	CLASS 375	SUBCLASS 354	ART UNIT 2637	EXAMINER Tse
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## APPLICANTS

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**TITLE**

Clock phase detecting circuit and clock regenerating circuit each arranged in receiving unit of multiplex radio equipment

PTO-2040  
12/89

☐ SURRENDER OF ORIGINAL PATENT \_\_\_\_\_ (Exr. Initials)      ORIGINAL PATENT NUMBER \_\_\_\_\_

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> <div> <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div> </div>		<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div> <div>_____ (Assistant Examiner) _____ (Date)</div> <div>_____ (Primary Examiner) _____ (Date)</div> <div>_____ (Legal Instruments Examiner) _____ (Date)</div> </div>		<b>NOTICE OF ALLOWANCE MAILED</b>	
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